

ReCoSoC

www.recosoc.org

12th International Symposium on Reconfigurable Communication-centric Systems-on-Chip



July 12-14, 2017, Madrid, Spain

Call for Papers

General Chair

Eduardo de la Torre
U. Politécnica de Madrid, ES

Program Chairs

Gilles Sassatelli
LIRMM, Montpellier, FR

Thilo Pionteck
Universität Magdeburg, DE

Andrés Otero
U. Politécnica de Madrid, ES

Program Committee

See www.recosoc.org

Local Organization

Centre of Industrial Electronics
Universidad Politécnica de Madrid, Spain

Contact

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Centre of Industrial Electronics
José Gutiérrez Abascal, 2
28006 Madrid, Spain

ReCoSoC has established itself as a consolidated international event, acting as a reference for researchers in the areas of reconfigurable and communication-centric systems-on-chip. Its informal and dynamic philosophy encourages technical and scientific discussions between researchers at very different levels of expertise: from senior academics to young researchers are welcomed to attend ReCoSoC.

The accepted papers will be submitted for inclusion to **IEEE Xplore**. The authors of the best papers will be invited to submit an extended version of their contributions to a special journal issue (*to be defined*).

Topics of interest:

The areas of interest include (but are not limited to) :

- New paradigms for reconfigurable and communication-centric computing
- Self-aware, Reconfigurable and adaptive embedded SoCs
- Communication-centric design techniques at different abstraction levels
- On-chip communication architectures
- Fault tolerance techniques for SoCs
- Low power design of reconfigurable and multiprocessor SoCs
- Communication-aware multiprocessor embedded systems
- OS and middleware for reconfigurable and multicore SoCs
- Specification languages and System design methodologies
- Verification and evaluation techniques
- Industrial case studies

Submission:

Authors are invited to submit a maximum 8 pages paper contribution in IEEE conference format. Contribution(s) have to be submitted electronically through the EasyChair portal of the conference:

<https://easychair.org/conferences/?conf=recosoc2017>

Important Dates: (*firm deadlines*)

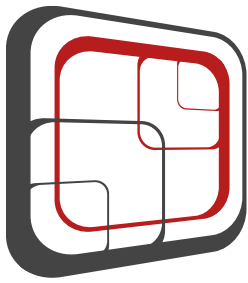
Abstract submission deadline :	31 st March 2017	21st April 2017
Full paper submission deadline :	7 th April 2017	28th April 2017
Notification of acceptance :	19 th May 2017	2nd June 2017
Camera-ready due :	12 th June 2017	19th June 2017



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Steering Committee

- Alberto Garcia Ortiz, *U. Bremen, DE*
- Manfred Glesner, *TU Darmstadt, DE*
- Thomas Hollstein, *Frankfurt U. of Applied Sciences, DE*
- Gert Jervan, *Tallinn TU, EE*
- Gilles Sassatelli, *LIRMM-CNRS, FR*
- Leandro Soares Indrusiak, *University of York, UK*
- Eduardo de la Torre, *U. Politécnica de Madrid, ES*
- Lionel Torres, *LIRMM-CNRS, FR*

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 28006 Madrid, Spain

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Special Sessions

The ReCoSoC technical program will incorporate Special Sessions, with the objective of complementing the regular program with new and emerging topics in the field of reconfigurable and communication-centric systems-on-chip. In 2017, ReCoSoC will have **three Special Sessions**:

Cutting Edge Heterogeneous Image Processing Systems

Organized by:

François Berry (Université Clermont-Auvergne), francois.berry@uca.fr
 Maxime Pelcat (INSA Rennes), mpelcat@insa-rennes.fr

High Level Design Methodologies for Reconfigurable Computing and Adaptive Systems: tool flows and applications

Organized by:

Rubén Salvador (Univ. Politécnica de Madrid), ruben.salvador@upm.es
 Eduardo Juarez (Univ. Politécnica de Madrid), eduardo.juarez@upm.es
 Jocelyn Sérot (Universite Blaise Pascal), jocelyn.serot@univ-bpclermont.fr

Fault and Security Management in NoCs and MPSoCs

Organized by:

Martha Johanna Sepulveda Florez (TU Munich), johanna.sepulveda@tum.de
 Thomas Hollstein (Frankfurt U. of Appl. Sciences/TU Tallinn), hollstein@fb2.fra-uas.de

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