

## REGISTRATION FORM

April 19<sup>th</sup>-20<sup>th</sup>, 2018

Name: \_\_\_\_\_

Family name: \_\_\_\_\_

Company/University: \_\_\_\_\_

Address: \_\_\_\_\_

City: \_\_\_\_\_ Postal Code: \_\_\_\_\_

Tel.: \_\_\_\_\_ email: \_\_\_\_\_

On Thursday morning you are invited to attend one or two short courses. Course A is running in parallel with Courses B and C that run sequentially. Please indicate if you would like to attend Course A or Courses B and/or C:

***COURSE A. Boosting Flexibility and Computing Performance in Dynamically Reconfigurable FPGA-Based Embedded Systems (PROVISIONAL TITLE)***

Coordinators: Andrés Otero & Alfonso Rodríguez  
Schedule: 9:00 at 13:00 h.

***COURSE B. GaN transistors as an enabler of high performance power electronics***

Coordinators: Miroslav Vasic & Pedro Alou (CEI)  
Schedule: 9:00 at 11:30 h

***COURSE C. EMI/EMC pre-compliance and debug with a modern oscilloscope***

Coordinator: Víctor Medina (Rohde&Schwarz)  
Schedule: 11:45 at 13:00 h

I will attend:

YES      NO

**Thursday afternoon**      April 19th

**Friday morning**      April 20th

**Friday cocktail**      April 20th

Please, send it to [cei@upm.es](mailto:cei@upm.es)

More information  
[http://www.cei.upm.es/annual-meeting/2018\\_edition/](http://www.cei.upm.es/annual-meeting/2018_edition/)

Universidad Politécnica de Madrid  
E.T.S. Ingenieros Industriales  
José Gutiérrez Abascal, 2 28006 Madrid  
Tel.: +34 913 36 31 94  
[cei@upm.es](mailto:cei@upm.es)